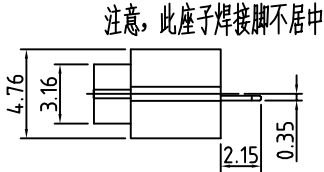
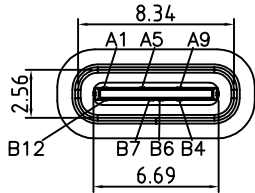
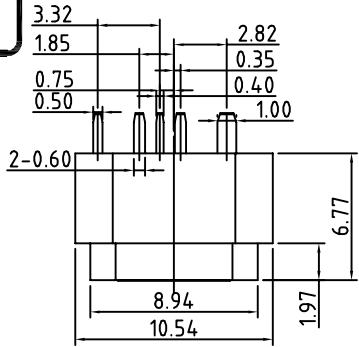


REV.	ECN. NO.	APPD.

CAD GENERATED DRAWING. DON'T CHANGE BY HAND.



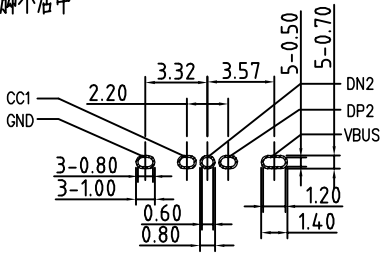
INTERFACE(FRONT VIEW)

A1	A2	A3	A4	A5	A6	A7	A8	A9	A10	A11	A12
GND				CC1				VBUS			
GND				DN2	DP2			VBUS			
B12	B11	B10	B9	B8	B7	B6	B5	B4	B3	B2	B1

PIN ASSIGNMENT

PIN NO.	A1&B12	A5	B7	B6	A9&B4
SIGNAL NAME	GND	CC1	DN2	DP2	VBUS
PCB PAD NO.	C1	C2	C3	C4	C5

注意,此座子焊接脚不居中



Recommended P.C.B. Layout
Unspecified tolerance: ± 0.05
(Top view)

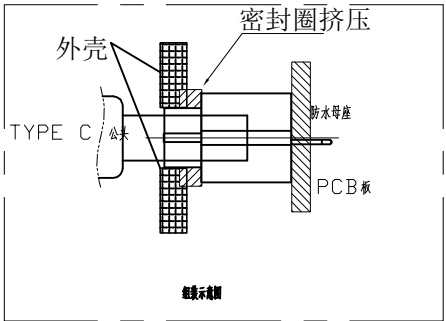
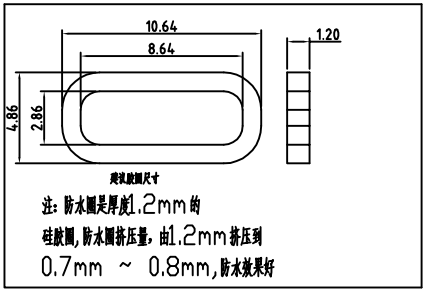
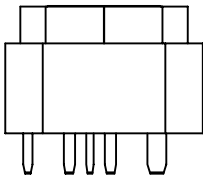
RECOMMENDED PCB LAYOUT(TOP VIEW)

THICKNESS T=1.0mm
DEFAULT TOLERANCE ± 0.05

NOTES:

SPECIFICATION:

- CURRENT RATING: 3.0A MAX
- DIELECTRIC WITHSTAND VOLTAGE: 100VAC
- TEMPERATURE RANGE: $-30^{\circ}\text{C} \sim 85^{\circ}\text{C}$
- CONTACT RESISTANCE: 40 MILLIONHM MAX
- INSULATION RESISTANCE: 100 MEGOHMS MIN.
- INSERTION FORCE : 5N~20N
- UNMATING FORCE: 8N~20N
- DURABILITY:5000 CYCLES



5	Shell2	1	SUS304 Ni Plating T=0.20mm	Ni40~120u'
4	Shell1	1	SUS304 Ni Plating T=0.3mm	Ni40~120u'
3	Housing2	1	LCP(G.F30%)	
2	Contact	4	Phosphor bronze C5191R-H Au/Ni Plating T=0.25mm	Ni40~160u'; AuCONTACT AREA:3u'
1	Housing	1	PA9T (G.F30%)	
ITEM	PARTNAME	Q'TY	FINISH	NOTE

X.	± 0.50	X.*	$\pm 2^{\circ}$	UNITS	mm
.X	± 0.30	.X.*	$\pm 1^{\circ}$		
.XX	± 0.10	.XX.*	$\pm 0.5^{\circ}$	MAT'L	
.XXX	± 0.05			FINISH	
				Q'TY	

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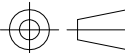
PART NO.
EZ-USBC-1148

TITLE:
TYPE C 7Pin(带5PIN) TYPE-C 转精

APPD: RADDM
CHKD: MARK

DWG NO.:
EZ-USBC-1148

DR: wu.jy 2022/02/02



SCALE	SHEET	REV.
1/1	1/2	A1